

Applicant: Kimsal et al.

Examiner: M Nguyen

Serial #: 09/039,344

Group Art Unit: 2816

: March 13, 1998 Filed

Docket: 12583.5-US-U1

: TIME INTERVAL MEASUREMENT SYSTEM INCORPORATING A LINEAR Title

RAMP GENERATION CIRCUIT

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service, as first class mail, in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on September 2, 1999.

Name: Robert J.

AMENDMENT AND RESPONSE

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

In response to the Office Action dated March 23, 1999, Applicants respectfully request entry and consideration of the following amendments, which, Applicants submit, place the application in condition for allowance.

AMENDMENTS

IN THE SPECIFICATION:

E SPECIFICATION:

At page 5, line 1 of the Brief Description of the Drawings, after "FIG. 1;" delete "arg"

At page 5, line 4, insert:

5, line 4, insert:

- Fig. 3A illustrates a source switching network and source control network